

Notice of References Cited	Application/Control No. 09/358,388		Applicant(s)/Patent Under Reexamination UMEZAWA ET AL.	
	Examiner Anh D. Mai		Art Unit 2814	Page 1 of 1

U.S. PATENT DOCUMENTS								
*	A	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
							APS	OTHER
<input type="checkbox"/>	A	5985735	Nov. 1999	Moon et al.	438	435	<input type="checkbox"/>	<input checked="" type="checkbox"/>
<input type="checkbox"/>	B	5496764	Mar. 1996	Sun	438	155	<input type="checkbox"/>	<input checked="" type="checkbox"/>
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FOREIGN PATENT DOCUMENTS									
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NON-PATENT DOCUMENTS				
*		DOCUMENT (including Author, Title Date, Source, and Pertinent Pages)	DOCUMENT SOURCE **	
			APS	OTHER
<input type="checkbox"/>	U	Wolf et al. "Silicon Processing for the VLSI Era" Vol. 1, 1986, Lattice Press, page 194	<input type="checkbox"/>	<input checked="" type="checkbox"/>
<input type="checkbox"/>	V		<input type="checkbox"/>	<input type="checkbox"/>
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*A copy of this reference is not being furnished with this Office action. (See Manual of Patent Examining Procedure, Section 707.05(a).)

**APS encompasses any electronic search i.e. text, image, and Commercial Databases.

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